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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/006,551	11/30/2001	Christopher D.S. Donham	NVIDP064/P000286	2643
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DATE MAILED: 09/30/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/006,551	Applicant(s) DONHAM ET AL.	
	Examiner Tam D. Tran	Art Unit 2676	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 August 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-30 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 4, 5, 8, 9, 18-21, 24-27, 30 are rejected under 35 U.S.C. 102(b) as being anticipated by Peterson et al. (USPN 5767856), hereinafter simply Peterson.

2. In regard to claim 1, 21, 24-27, 30, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, see Fig.2, comprising: (a) sending an instruction request to video memory (command queue sending instruction request to bilinear texture, bilinear texture sending instruction request to read req queue wherein read req queue correspond to video memory), where a texture module in a graphics pipeline sends the instruction request to the video memory; see Fig.2, col.3 lines 62-67; and (b) receiving instructions from the video memory in response to the instruction request utilizing the texture module in the graphics pipeline, (attribute queue receiving instructions to output from pixel pipeline). See Fig.2, col.4 lines 60-65.

3. In regard to claim 4, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the memory includes a frame buffer (video memory). See col.5 lines 3-7.

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4. In regard to claim 5, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the memory includes direct random access memory (DRAM). See Fig.1.

5. In regard to claim 8, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, and further comprising receiving initial instructions from a rasterizer module (graphic processor) coupled to the texture module. See Fig.1.

6. In regard to claim 9, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the initial instructions control at least the sending of the instruction request by the texture module (output of pixel engine are sent to write data queue). See col.5 lines 7-15.

7. In regard to claim 18, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein a complete instruction set is received in response to the instruction request. See Fig.2.

8. In regard to claims 19, 20, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein a partial instruction set is received in response to the instruction request. See Fig.2.

Claim Rejections - 35 USC § 103

9. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 2, 3, 6, 7, 10-17, 22, 23, 28, 29, are rejected under 35 U. S.C. 103(a) as being unpatentable over Peterson et al. (USPN 5767856) in view of Applicant Admitted Prior Art (AAPA).

10. In regard to claim 2, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, Peterson does not teach a method of sending a texture request to memory utilizing the texture module in the graphics pipeline. However, AAPA teaches a method of sending a texture request to memory utilizing the texture module in the graphics pipeline See Fig.2. It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate the method of sending a texture request of AAPA into texture module of Peterson because a combination of AAPA's method and Peterson's texture module would provide a communication channel between memory and texture module for transferring texture data from the memory to back end of pixel pipeline.

11. In regard to claim 3, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, and further comprising receiving texture information from the memory in response to the texture request utilizing the texture module in the graphics pipeline. See Fig.2.

12. In regard to claim 6, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the instructions are adapted for controlling a texture environment module (write data queue) coupled to the texture module. See Fig.3.

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13. In regard to claim 7, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the instructions control the manner in which the texture environment module (write data queue) processes the texture information. See col.5 lines 7-24.

14. In regard to claim 10, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, and further comprising temporarily storing the instructions and the texture information in cache. See Fig.2.

15. In regard to claim 11, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the cache is resident on the texture module. See Fig.2.

16. In regard to claim 12, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein each piece of texture information and each of the instructions are of a similar size in the memory. It is inherent that data store in memory have similar size. See Fig.2.

17. In regard to claim 13, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, and further comprising controlling the texture module utilizing a shader module coupled thereto. See Fig.3.

18. In regard to claim 14, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the shader module controls the sending of instruction request and texture request by the texture module. See Fig.3
page 5 lines 24-31.

19. In regard to claim 15, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the shader module processes a plurality of pixels with the texture information based on the instructions. See Fig.3.

20. In regard to claim 16, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, wherein the shader module is capable of reusing the texture information in order to request further texture information from the video memory (control the looping of texture process), see Fig.3 page 4 lines 24-31.

21. In regard to claim 17, AAPA teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, and further comprising ceasing the processing upon the receipt of terminate instruction (require significant amount of time to push down the pipeline). See Fig.3 page 5 lines 7-15.

22. In regard to claims 22, 23, Peterson teaches a method and system for retrieving instructions from memory utilizing a texture module in a graphics pipeline, Peterson does not teach texture module is adapted for operating in a plurality of different modes. However, AAPA teach texture module is adapted for operating in a plurality of different modes. See page 3 lines 20-25. It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate texture module of Peterson into the texture module of AAPA because a combination of texture module operating in plurality of difference modes and the texture module of Peterson would provide components of the texture module processing the texels in various ways such as an address calculation module allow various dimensionality textures.

23. In regard to claims 28, Peterson teaches a method for retrieving instructions from memory, comprising:(a) receiving a plurality of preliminary instructions from a rasterizer

module utilizing a texture module coupled thereto; see Fig.2, Fig.3, col.5 lines 1-10; (b) sending an instruction request to memory utilizing a texture module coupled to the texture module; see Fig.2, col.4 lines 59-67; (d) caching the additional instructions on the texture module; see Fig.1; Peterson does not teach (e) sending a texture request to memory utilizing the texture module in accordance with the additional instructions; (f) receiving texture information from the memory in response to the texture request utilizing the texture module; (g) caching the texture information on the texture module; (i) repeating (b) - (g) in accordance with the additional instructions.

However, AAPA teaches (e) sending a texture request to memory utilizing the texture module in accordance with the additional instructions; (f) receiving texture information from the memory in response to the texture request utilizing the texture module; (g) caching the texture information on the texture module; (i) repeating (b) - (g) in accordance with the additional instructions. See Fig.2. It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate the method of sending a texture request of AAPA into texture module of Peterson because a combination of AAPA's method and Peterson's texture module would provide a communication channel between memory and texture module for transferring texture data from the memory to back end of pixel pipeline.

24. In regard to claim 29, Peterson teaches a method for retrieving instructions from memory, comprising:(a) receiving a plurality of preliminary instructions from a rasterizer module utilizing a texture module coupled thereto; see Fig.2, Fig.3, col.5 lines 1-10; (b) sending an instruction request to video memory, where a texture module sends the instruction request to the video memory; see Fig 2, col.4 lines 59-67; (d) caching the additional instructions on the texture module; see Fig.1; Peterson does not teach (b) sending an instruction request to memory utilizing

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the texture module couple to shader module; (e) sending a texture request to memory utilizing the texture module in accordance with the additional instructions; (f) receiving texture information from the memory in response to the texture request utilizing the texture module; (g) caching the texture information on the texture module; (i) repeating (b) - (g) in accordance with the additional instructions; (j) outputting the processed pixels upon receipt of additional instructions that include a terminate instruction. However, AAPA teaches (b) sending an instruction request to memory utilizing the texture module couple to shader module; see Fig.3; (e) sending a texture request to memory utilizing the texture module in accordance with the additional instructions; (f) receiving texture information from the memory in response to the texture request utilizing the texture module; (g) caching the texture information on the texture module; (i) repeating (b) - (g) in accordance with the additional instructions. See Fig.2. (j) outputting the processed pixels upon receipt of additional instructions that include a terminate instruction (require significant amount of time to push down the pipeline). See Fig.3 page 5 lines 7-15. It would have been obvious to a person of ordinary skill in the art at the time of the invention to incorporate the method of sending a texture request of AAPA into texture module of Peterson because a combination of AAPA's method and Peterson's texture module would provide a communication channel between memory and texture module for transferring texture data from the memory to back end of pixel pipeline.

Response to Arguments

25. Applicant's arguments filed on (08/03/2005), have been fully considered but they are not persuasive.

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Applicant argues that the prior art does not teach “receiving instructions from the video memory in response to the instruction request utilizing the texture module in the graphic pipeline”

However, examiner respectfully disagrees with the argument because Fig.2, col.3 lines 62-67, Peterson teaches command queue sending instruction request to bilinear texture (texture module), bilinear texture sending instruction request to read req queue wherein read req queue corresponds to video memory.

Applicant argues that the prior art does not teach “instruction set”. However, examiner respectfully disagrees with the argument because examiner did not see the term “instruction set” being claimed by applicant. Do instruction set and instructions have the same meaning?

Peterson teach command including instructions, see col.3 lines 63-66.

For these reasons, the rejections are maintained.


26. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Conclusion

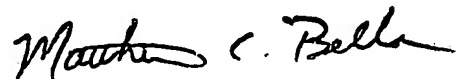
27. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Tam D. Tran** whose telephone number is **571-272-7793**. The examiner can normally be reached on MON-FRI from 8:30 – 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Matthew Bella** can be reached on **571-272-7778**. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tam Tran

Examiner

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MATTHEW C. BELLA
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600